

All Sciences Proceedings <u>http://as-proceeding.com/</u> 2nd International Conference on Engineering, Natural and Social Sciences

April 4-6, 2023 : Konya, Turkey



© 2023 Published by All Sciences Proceedings

https://www.icensos.com/

A new view on image-force lowering effect in Schottky junctions

Murat GÜLNAHAR^{*1}

¹Department of Electrics/Vocational School, Erzinczn Binali Yıldırım University, Turkey

*mgulnahar@erzincan.edu.tr

Abstract – In this paper, the image force lowering effect from Schottky barrier anomalities in metalsemiconductor contacts has been viewed in based of an Al/p-Si Schootky junction and has been presented a new consideration that is; the dielectric constant should be a function of the temperature. Thus, it was seen that the image force lowering values in two separate temperature have increased in the low temperature despite decrease for $\varepsilon_s = 11.8$ constant value at 300 K. In addition, the values of the image force lowering have been obtained as 33.0 meV for 200 K and 16.0 meV for 300 K.

Keywords – Schottky Diodes, Schottky Baarier Inhomogeneities, Dielectric Constant, Image-Force Lowering Effect, Si

I. INTRODUCTION

The interface layer in which is between the metal and semiconductor is an effective layer in Schottky junctions and it affects the stability, reliability and performance of the diode. The position of the electrical charges existing in the interface, the quality composition, and non-stoichiometric structure of the interface induce the interface states. Schottky barrier anomalies in barrier are due to the interface states and they are effective on the other all barrier parameters such as ideality factor and barrier height. It is known that a device which does not have an interface with an abrupt junction is under the ideal contact conditions, and in these cases that ideality factor is 1 and the barrier height is constant; its current mechanism is through of the thermionic emission. However, the current transport mechanism under non-ideal conditions deviates from the thermionic emission theory.

So far, the a lot of the experimental studies have presented that the measured I-V curves demonstrate that there are excess currents, which arise from some current transport mechanisms such as thermionic-field emission and field emission additionally to the thermionic emission current in the junction [1-5]. These excess currents can flow through the barrier and it depends on characteristic parameters such as the temperature of the device, the concentration of dopants, the applied bias, the properties of junction and interface. Consequently, due to these anomalies, ideality factor has the bigger values from one and the barrier height is not stable as a function of the temperature, as the barrier parameters [1].

The other parameter influenced from Schottky barrier inhomogeneities is image force lowering effect. The image-force lowering is a lowering of metal-semiconductor barrier arising from image force interaction with the field at their interface. The barrier height according to image-force lowering effect is written as [1,2],

$$\phi_b = \phi_b^0 - \Delta \phi_b \tag{1}$$

where ϕ_b is the barrier height, ϕ_b^0 is the zero bias barrier height and $\Delta \phi_b$ is the barrier lowering due to image force lowering effect. Thus, $\Delta \phi_b$ is given as [1,2],

$$\Delta \phi_b = \left[\frac{qN_i}{8\pi^2 \varepsilon_s^3} \left(\phi_b - V - \epsilon - \frac{kT}{q}\right)\right]^{1/4} \tag{2}$$

where \in is the energy difference between the Fermi level and the bottom of the conduction band or the top of the valence band, *V* is the applied bias, ε_s is semiconductor dielectric constant, N_i is the net ionized state concentration. The interface region demonstrated the dielectric oxide layer property between metal with semiconductor in the junction plays the important roles over the junction structure and parameters. Moreover, the temperature behavior and the structure of this oxide layer are not known sufficiently. Besides, ε_s semiconductor dielectric constant is related to with behaviors of interface layer and the depletion layer.

So far, in experimental studies upon metalsemiconductor junctions to calculate the imageforce lowering effect, ε_s semiconductor dielectric constant has been considered as a constant parameter that is independent from the temperature [1-9] and as a result, it has been seen that the imageforce lowering values decrease with decreasing temperature [8]. Finally, it has been reported that Eq. (2) is unsuccessful [7-9].

 ε_s semiconductor dielectric constant plays an important role in correctly determination of imageand N_i net ionized force lowering state concentration values as seen in Eq. (2). In this paper is suggested a technique to determine ε_s dielectric constant for anyone measurement temperature in metal semiconductor contact structures. The obtained equation and the method were used for 200 K and 300 K temperatures of an Al/p-Si sample [10] and their ε_s dielectric constant and image-force lowering values were calculated in this research.

II. MATERIALS AND METHOD

Basic Equations

However, in literature on the metal-semiconductor contact structures is widely known that the effect of interface anomalies increase with decreasing temperature and consequently the image-force lowering values must increase in the low temperatures [1,2]. Therefore, to consider as an independent parameter from the temperature in Schottky contacts is not meaningful for ε_s . In a lot of thin film studies separately from the metalsemiconductor contacts. \mathcal{E}_{S} semiconductor dielectric constant values can be calculated as a function of the temperature using the different charge-transport mechanisms [11-14].

The charge-transport mechanisms in various materials are explained with models such as Schottky, Pool-Frenkel and the charge-limited conduction mechanisms [1,2,6-18]. It is known that Schottky effect and Pool-Frenkel effect are the mechanisms used frequently in thin dielectric films and in the semiconductors [1,2,6-17].

Schottky effect is the image force lowering effect arisen from a process occurring in the interface of the metal with semiconductor (or insulating film) as a result of the image force and the applied bias effect [1,2]. Moreover, Schottky conduction is an electronlimited conductivity mechanism that occurs with fewer defects. However, Pool-Frenkel effect has an analogy effect to well-known Schottky effect. Pool-Frenkel effect occurs with field-assisted thermal ionization and it is a Coulombic potential barrier lowering [18]. In experimental studies, to comment the current transport properties in reverse direction of the diodes is reported that Pool-Frenkel effect is a dominant current transport mechanism in reverse direction [11-13]. Therefore the general I-V expression of Pool-Frenkel effect can be written as [19],

$$I = AA^* exp\left(-\frac{q\phi_b}{kT}\right) exp\left(\frac{q\beta_{PF}}{kT}\sqrt{\frac{V_r}{d}}\right)$$
(3)

where V_r is the applied reverse bias, β_{PF} is Poole-Frenkel constant known as $\beta_{PF} = (q/(\pi \varepsilon_0 \varepsilon_s))^{1/2}$ and d is width of the depletion layer and it is written as,

$$d = \left(\frac{2\varepsilon_0\varepsilon_s(V_0 - V_r)}{qN_i}\right)^{1/2} \tag{4}$$

where V_0 is the diffusion potential which is determined from extrapolation of a linear $1/C^2$ - V_r plot.

In reverse direction in experimental studies made upon thin solid films [11-13] and metalsemiconductor contact [20] is expressed a linear behavior in the curves when is plotted the reverse current versus in form of $V_r^{1/2}$ applied bias. In consequence, experimental ε_s dielectric constant for thin films is obtained as a function of the temperature with using as *d* sample thickness from slope of this plot [11-13]. However, experimental ε_s dielectric constant for metal-semiconductor contact structures is not obtained using this method and it is reported as a constant parameter for everyone semiconductor sample [1,2,6-9]. The semiconductor depletion layer in Schottky diodes yields the differential capacitance. According to the depletion approximation, the measured differential capacitance is given as [19],

$$\frac{1}{C^2} = \left(\frac{2(V_0 + V_r)}{q\varepsilon_s N_i A^2}\right) \tag{5}$$

N_i net ionized state concentration may be calculated classically from slope of C⁻²-V_r plot with using a constant value of ε_s for semiconductor. However, if one considers that ε_s should vary as a function of the temperature separately from this consideration, then Eq. (6) may be re-considered as,

$$\varepsilon_s N_i = \left(-\frac{2}{qA^2 \varepsilon_0 \frac{d(1/C^2)}{dV}} \right) \tag{6}$$

If one use in Eq. (3) to Eq. (4) with Eq. (6), then Eq. (3) may be re-written as,

$$lnI = lnI_{PF} + \frac{q\beta_{PF}}{kT\varepsilon_{s}^{\frac{1}{2}}(\frac{2\varepsilon_{0}}{qm})^{\frac{1}{4}}} \left(\frac{V_{r}}{(V_{0} - V_{r})^{\frac{1}{2}}}\right)^{\frac{1}{2}}$$
(7)

Therefore, according to Eq. (7) may be plotted ln *I* versus $(V_r/(V_0 - V_r)^{1/2})^{1/2}$ variation and experimental ε_s dielectric constant from slope of this plot may obtained.

III. RESULTS AND DISCUSSIONS

To obtain ε_s dielectric constant values from Eq. (7) was used I-V_r and C-V_r measurements for 200 K and 300 K temperatures of a Al/p-Si sample.

Fig. 1 shows C⁻²-V_r plot for 200 K and 300 K temperatures of Al/p-Si. In Fig. 1 C⁻²-V_r plot is fairly linear for two temperature values. This linear variations present that is low of effects as the series resistance, the deep levels which are electrically active in semiconductor and the minority carriers [20]. From Fig.1 V₀ diffusion potential and N_i net ionized state concentration values was calculated as 0.97 eV, 9.28x10¹⁵ cm⁻³ for 200 K and 0.81 eV, 9.29x10¹⁵ cm⁻³ for 300 K respectively.

Fig. 2 presents $\ln I$ versus $(V_r/(V_0 - V_r)^{1/2})^{1/2}$ plot for 200 K and 300 K temperatures of Al/pSi. As seen in Fig. 2, this variation is fairly linear for two temperature values. ε_s values using Eq. (7) was calculated as 8.21 for 200 K and 11.04 for 300 K. This value is reported as 11.8 for Si [2]. The



Figure 1. Plot of C⁻² versus V_r in 200 K and 300 K for a Al/p-Si sample [16].

calculated 11.04 value for ε_s value in 300 K is near to 11.8 value of Si. Besides, the barrier height



Figure 2. Curves of semi-log(I) versus $\forall_r^{1/2}/(\bigvee_0 - \bigvee_p)^{1/4}$ in 200 K and 300 K for a Al/p-Si sample [16] and their linear fits.

parameters as the N_i net ionized state concentration and \emptyset_b barrier height values with aiding Eq. (7) using 8.21 and 11.04 values for ε_s was calculated as 3.60×10^{15} cm³, 1.08 eV for 200 K and as 9.35×10^{14} cm⁻³, 0.95 eV for 300 K, respectively. However N_i and \emptyset_b values was obtained as 7.87×10^{14} cm⁻³ and 1.11 eV for 200 K and as 7.87×10^{14} cm⁻³ and 1.02 eV for 300 K classically using $\varepsilon_s = 11.8$ value of Si.

Image-force lowering values may be calculated using Eq. (2). In consequence, it was obtained as 13.1 meV for 200 K and as 13.7 meV for 300 K with 11.8 constant value of ε_s for Si using Eq.(2) from Fig.1. However, image-force lowering values was calculated as 33.0 meV for $\varepsilon_s = 8.21$ in 200 K and 16.0 meV for $\varepsilon_s = 11.04$ in 300 K with aiding above expressed new method from Fig.1 and Fig.2.

IV.CONCLUSION

In metal-semiconductor contact studies is a reality that the effect of barrier inhomogeneities have increased in the low temperatures. Therefore is expected that the image force lowering values increase in low temperatures such as behavior of the ideality factor.

In this study was reported a new idea for the behavior of the image force lowering in two separate temperate using an Al/p-Si sample. In consequence was demonstrated that ε_s should be as a function of the temperature. Therefore was seen that the image force lowering values in two separate temperature have increased in the low temperature despite decrease for $\varepsilon_s = 11.8$ constant value. To present as the more general to behavior of ε_s and the image force lowering effect in different temperatures is not clear at the present study and further experiments are in progress.

References

[1] E. H. Rhoderick and R. H. Williams. *Metal-Semiconductor Contacts*, 2nd ed. Oxford: Clarendon. 1988.

[2] S. M. Sze. *Physics of Semiconductor Devices*, Second Edition, John Wiley and Sons: New York, 1981.

[3] M. Gülnahar, *International Conference on Scientific and Academic Research 1*, vol. 1, pp. 287-291, 2023.

[4] M Gülnahar, International Conference on Scientific and Academic Research 1, vol. 1, pp. 292-295, 2023.

[5] M Gülnahar, International Conference on Scientific and Academic Research 1, vol, 1, pp. 472-475, 2023.

[6] V. L. Rideout and C. R. Crowell. *Solid State Electronics*, vol. 13, pp. 9931009, 1970.

[7] S. Chand and J. Kumar. *Semiconductor Science and Technol*, vol. **10**, pp. 1680-1688, 1995.

[8] M. P. Hernandez, C. F. Alonso and J. L. Pena. *J Physics D:Applied Physics*, vol. 34, pp. 1157-1161, 2001.

[9] B. Abay, G. Çankaya, H. S. Güder, H. Efeoğlu, and Y. K. Yoğurtçu. *Semicond Sci and Technol*, vol. 18, pp. 75-81, 2003.
[10] M. Gülnahar, H. Efeoğlu. *I-V_r and C-V_r measurements for 200 K and 300 K of Al/p-Si*, Unpublished; 2007.

[11] C. K. Maiti, S. Maikap, S. Chatterjee, S. K. Nandi, S. K. Samanta. *Solid State Electronics*, vol. 47, pp. 1995-2000, 2003.

[12] S. Chakraborty, M. K. Bera, S. Bhattacharya, C. K. Maiti. *Microelectronic Engineering*, vol. 81, pp. 188-193, 2005.

[13] G. K. Dalapati, S. Chatterjee, S. K. Samanta, S. K. Nandi, P. K. Bose, S. Varma, S. Patil, C. K. Maiti. *Solid State Electronics*, vol. 47, pp. 1793-1798, 2003.

[14] T. S. Shafai, T. D. Anthopoulos. *Thin Solid Films*, vol. 398-399, pp. 361-367, 2001.

[15] J. G. Simmons. J Physics D: Applied Physics, vol. 4, pp. 613-657, 1971.

- [16] J. G. Simmons. *Physical Review*, vol. 155, pp. 657-660, 1967.
- [17] J. Frenkel. Phys. Rev., vol. 54, pp. 647-648, 1938.
- [18] A. E. Rakshani, Y. Makdisi, X. Mathew, and N. R.

Mathews. *Phys. Stat. Sol.* (*a*), vol. 168, pp. 177-187, 1998. [19] S. J. Fonash. *J. Appl. Phys.*, vol. 54 (4), pp. 1966-1975, 1983.

[20] P. K. Vasudev, B. L. Mattes, E. Pietras and R. H. Bube. *Solid State Electronics*, vol. 19, pp. 557-559, 1976.