

# On analysis of current-voltage characteristics in an inhomogeneous Al/4H-SiC junction structure

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Abstract – In this paper, the current-voltage (I-V) measurements of an Al/4H-SiC Schottky device are characterized as a function of the temperature in 60-300 K temperature range. The series resistance values are calculated to be 12.5  $\Omega$  at 300 K from Cheung functions. The experimental parameters such as the ideality factor and apparent barrier height show resolute temperature dependent. These inhomogeneous characteristics observed for Al/4H-SiC are related to Schottky barrier anomalities. Thus, it may be commented that the temperature dependent electrical characteristics of an Al/4H-SiC Schottky sample can be characterized on the basis of the thermionic emission theory.

Keywords - 4H-Sic, Schottky Diodes, Schottky Barrier Inhomogeneities, Ideality Factor, Barrier Height

## I. INTRODUCTION

The interface layer between the metal and semiconductor affects the stability, reliability and performance of the diode. The composition, quality and non-stoichiometric structure of the interface and the position of electrical charges in the interface are known as interface states. The interface states cause Schottky barrier anomalies in barrier and influence the other all barrier parameters such as ideality factor and barrier height.

A contact structure which does not have an interface with an abrupt junction has ideal conditions and its current mechanism is governed by thermionic emission. Under these conditions, it is expected that ideality factor is 1 and the barrier height is constant. However, the current transport mechanism under non-ideal conditions cannot be described by the thermionic emission theory. The measured I-V curves demonstrate that there are excess currents, which arise from some current transport mechanisms such as thermionic-field emission and field emission as well as thermionic emission current in the barrier [1-9]. These current transport mechanisms can flow through the junction and depend on characteristic parameters such as the temperature of the device, the concentration of dopants, the applied bias, the properties of barrier and interface. As a consequence of these effects, ideality factor deviates from one and the barrier height is not constant over a broad temperature [1,2,7-18].

The effects of the non-homogeneous dielectric interface layer are introduced generally within ideality factor. The reason that is bigger than 1 is attributed to factors such as generationrecombination currents in the depletion region [1], tunnelling currents in highly doped semiconductors [1], interface states at a thin layer between the metal and semiconductor [19-21], image-force lowering of the Schottky barrier in an electric field at the interface [5].

SiC belongs to IV-IV group in the periodic table and it is an indirect wide band-gap semiconductor material. Due to some useful capabilities existing potentially of the electronic devices based on SiC such as high electron saturation drift velocity, a high thermal conductivity, a wide band-gap of 2.9 eV and large breakdown electric field, these devices have special importance technologically [22-24]. In addition, due to these excellent structural capabilities of SiC such as the large Si-C bonding energy, resistant to chemical attacks and radiation effects and its stability at high temperatures, it has been an attractive material for high power-high voltage switching applications, high power microwave applications, high radiation environments, high temperature electronics, high corrosion and high frequencies device applications [25-27].

In recently, the materials based on SiC Schottky rectifiers have been the charming electronic devices and they have been carried out a lot of works over their electrical transport properties. Moreover, in the literature, it is seen frequently that the researches connected with SiC Schottky rectifiers have concentrated on 4H-SiC [28-33] by comparison the other SiC based materials such as 6H-SiC.

So far, in the literature, it is seen that the separate metals such as tungsten (W) [34], titanium (Ti) [35-39], nickel (Ni) [35,37,40,41], molybdenum (Mo) [42,43] the have been used to fabricate Schottky contacts with 4H-SiC. As our knowledge, Al as a refractory metal on 4H-SiC was carried out in limited number [44,45]. In this study was carried out an analysis over I-V-T measurements of an Au/4H-SiC Schottky diode fabricated. Moreover, this paper relates to the behavior of the interface states in Au/4H-SiC and is analyzed the behaviors of the Schottky barrier inhomogeneities in this sample.

### **Experimental Details**

In this study, the n-type 4H-SiC bulk wafer material which is one-side polished used for Schottky diodes was purchased from commercially available Cree Research Inc. This wafer has <0001> crystalline orientation with carrier concentration of  $1.0 \times 10^{17}$  cm<sup>-3</sup>. It may get damaged due to mechanical polishing with diamond paste in decreasing grid sizes, the surface and subsurface of SiC and this result may cause in a specular surface on the macroscopic scale [46]. Thus, after annealing at 1100 °C in 30 min under Ar:%10H<sub>2</sub> gas flow, its surface in order to reconstruct atomically flat 4H-SiC was subjected to wet KOH etching.

Before the metallization deposition procedure, 4H-SiC wafers to remove the organic and inorganic contamination from the sample surface were exposed to classical Si cleaning process well known as RCA1 and RCA2. The ohmic contact operation on back of 4H-SiC sample was realized by thermal evaporation of Ni at 8x10<sup>-8</sup> Torr base pressure, and afterwards it was exposed to the annealing process at 950 <sup>o</sup>C temperature for 10 min under a N<sub>2</sub> gas flow. Schottky metallization process onto the other surface of the sample was made by thermal evaporation operation of Al at  $2x10^{-8}$  Torr base pressure. The current-voltage (I-V) measurements against the temperature for the 4H-SiC diodes were performed using a closed cycle He cryostat with 10 K temperature steps at 60-300 K temperature range stabilized in 50 mK with a LakeShore 330 temperature controller for measurements. The voltage for measurements was applied to sample with 5 mV steps driven from a Keithley 2400, and in result, the current was measured from a Keithley 6514 electrometer.

#### II. MATERIALS AND METHOD

According to the thermionic emission theory, the relation between the applied bias and thermionic emission current for is given as follows [47],

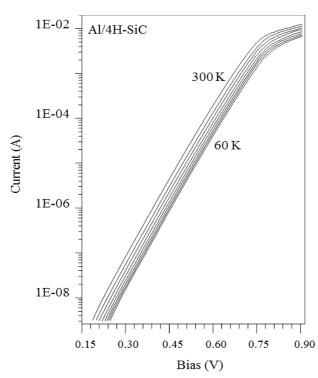
$$I = Io[exp(q(V - IRs)/nkT)]$$
(1.a)

with

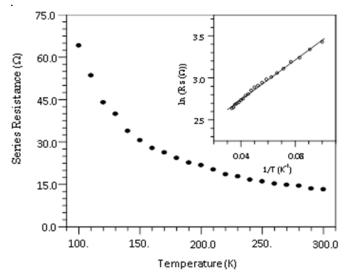
$$Io = AA * Texp(-q\phi/kT)$$
(1.b)

where T is the absolute temperature, k is the Boltzmann constant, q is the electronic charge, V is the applied voltage, n is the ideality factor, Rs is the series resistance,  $\phi$  is the apparent barrier height, A is the diode area and A\* is the Richardson constant.

Figure 1 presents the forward bias I-V characteristics of the Au/4H-SiC Schottky sample and it is seen that the measurements depend on the temperature with 30 K temperature steps over the 60-300 K temperature range and between 0 V and



**Figure 1.** Semi-logarithmic forward bias current-voltage characteristics of an Al/4H-SiC Schottky barrier diode. Temperature step is 30 K. A temperature stability less than 50 mK is provided for all measurements.



**Figure 2.** The series resistance values calculated for 100-300 k temperature range from Cheung functions of Au/4H-SiC. In the inset, the plot of In(R<sub>2</sub>) versus 1/T for this sample is presented.

1.5 V voltage values. In Fig. 1, it may be expressed that I-V characteristics of Al/4H-SiC have high rectification ratio and large thermionic emission region for all temperatures.

The series resistance effect in Schottky diodes always exists and it gets effective with increasing forward bias. In addition, it has an effect which limits the current. Cheung functions [48] in order to calculate the series resistance values of Schottky diodes, are preferred frequently in the literature. The variation versus temperature of series resistance values of Al/4H-SiC are presented in Figure 2 and they increase from 12.5  $\Omega$  to 65.3  $\Omega$  with the temperature which goes from 300 K to 100 K. Moreover, in the inset of this figure is presented the ln(R<sub>s</sub>) versus 1/T plot. It is expressed that the variation with temperature of the series resistance is due to the lack of free carrier concentration at low temperatures and the increase in ideality factor [14].

The bulk resistance in semiconductors is related to by the following equation [49],

$$1/Rs \circ \sigma = Cexp(-Ea/kT)$$
(2)

where Ea is the activation energy and C is a constant. Al/4H-SiC has an activation energy value of Ea:23.15 meV, calculated using Eq. (2) from the inset of Fig. 2.

From Eq. (1.a) for forward bias I-V curves, the ideality factor and apparent barrier height values may be written through the Eqs. (3.a) and (3.b)

$$n = q/kT \ dV/(d(lnI)) \tag{3.a}$$

$$\phi = -kT/q \ln(Io/(AA * T2))$$
(3.b)

The values of n and  $\phi$ , from Eqs. (3.a) and (3.b) were calculated as 1.28 and 0.99 eV at 300 K and as 1.75 and 0.87 eV at 150 K, respectively. Fig. 3 and Fig. 4 show the plots of  $\phi$  and n values versus temperature in 100-300 K temperature range.  $\phi$ values decrease while n increases swiftly with the temperature. On the other hand, in the literature, the relation with temperature of energy band gap of 4H-SiC, it is given as [50].

$$Eg = 3.19 - 3.3x10 - 4(T - 300) \tag{4}$$

The values of Eg is depicted addition to  $\phi$  values at 100-300 K temperature range in Fig.3 for Al/4H-SiC sample. In an ideal Schottky contact, the barrier height should increase essentially with temperature due to variation of band gap [46]. In the literature, these anomalities observed for the values of  $\phi$  in Fig.3 and n in Fig. 4 are related to Schottky barrier inhomegeneities [7-18].

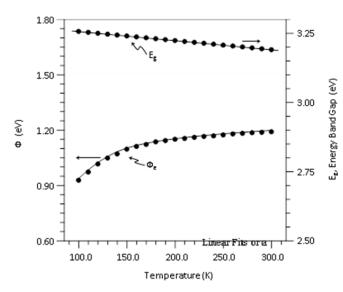


Figure 3. Temperature dependence of  $\phi_e$  and  $E_g$  energy band gap values calculated for temperature range of 100-300 K from I-V data,

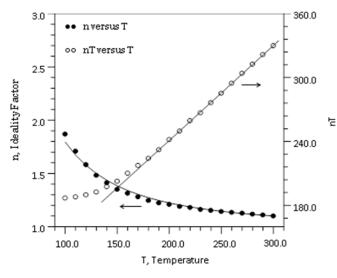


Figure 4. The plots of n versus T and nT versus T in 100-300 k temperature range. In addition, a discrete linear fit is carried out for the variation of nT versus T.

Furthermore, the plot of nT versus n is the one from possibilities determining the current transportation mechanism in a Schottky sample. According to this

consideration, the relationship with temperature of nT is expressed as,

$$nT = noT + To \tag{5}$$

From Eq. (5), the phenomenon which explains this relation is generally known as To effect. According to the plot of nT versus T, in case of the slope of this plot should have a value which is no=1, the conduction mechanism of diode is through the thermionic emission. But, when no is not close to 1, it may be explained that the conduction mechanism is through of the thermionic-field emission or field emission. From Fig. 4, no and To values were calculated as no:0.94 and To:41.68 K, respectively. In addition, the nT values for a wide temperature range in Fig. 4 are the linear and it deviates in low temperatures.

As seen in Figs. 3 and 4, at low temperatures, a resolute decrease in the apparent barrier height and increase in the ideality factor are related to the causations such as interfacial reactions, anomality thickness and composition of the interface layer, abnormality interfacial charges, of interface roughness, inhomogeneous doping, structural defects in the semiconductor, diffusion or interdiffusion of the metal contaminations applied on semiconductor substrate and existence of a thin insulating oxide layer in the junction [10,11]. In the literature, even in the most carefully processed samples, it is expressed that the fluctuations in barrier heights are unavoidable [51].

#### III. CONCLUSION

The current transport properties of an Al/4H-SiC Schottky sample have been characterized by means of I-V measurements in 60-300 K temperature range. The series resistance values were calculated to be  $12.5 \Omega$  at 300 K to be  $65.3 \Omega$  at 100 K by using Cheung functions. It was seen that the barrier height values decrease and ideality factor values increase as a function of temperature. These abnormalities calculated for the ideality factor and barrier height of I-V data were attributed to Schottky barrier anomalities existing in its barrier structure. The value of *To* as a result of the curve of experimental *nT* versus *T* was obtained to be *To*:41,68 K.

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#### References

[1] E. H. Rhoderick and R. H. Williams, *Metal-Semiconductor Contacts*, 2nd ed., Oxford: Clarendon, 1988.

[2] S. M. Sze, *Physics of Semiconductor Devices*, 2nd ed., John Wiley and Sons, New York, 1981.

[3] F. A. Padovani and R. Stratton, "Field and thermoionic-field emission in Schottky barriers," *Solid State Electronics*, vol. 9, pp. 695-707, 1966.

[4] F. A. Padovani, "Thermionic emission in Au-GaAs Schottky barriers," *Solid State Electronics*, vol. 11, pp. 193-200, 1968.

[5] VL. Rideout, CR. Crowell, "Effects of image force and tunneling on current transport in metal-semiconductor (Schottky barrier) contacts," *Solid State Electronics*, vol. 13, pp. 993-1009, 1970.

[6] J. Osvald and E. Dobrocka, "Generalized approach to the parameter extraction from I-V characteristics of Schottky diodes," *Semiconductor Science and Technology*, vol. 11, pp. 1198-1202, 1996.

[7] M. Gülnahar, H. Efeoğlu, "I-V-T characteristics of Al/p-GaTe Schottky contact structure," *Balkan Physics Letters*, vol. TFD-24, pp. 542-550, 2008.

[8] M. Gülnahar, "Electrical Characteristics of an Ag/n-InP Schottky diode based on temperature-dependent currentvoltage and capacitance-voltage measurements," *Metallurgical and Materials Transactions A-Physical Metallurgy and Materials Science*, vol. 9, pp. 3960-3971, 2015.

[9] M. Gülnahar, T. Karacali, H. Efeoğlu, "Porous Si based Al Schottky structures on p(+)-Si: A possible way for nano Schottky fabrication," *Electrochimica Acta*, vol. 168, pp. 41-49, 2015.

[10] J. H. Werner and H. H. Güttler, "Barrier inhomogeneities at Schottky contacts," *Journal of Applied Physics*, vol. 69(3), pp. 1522-1533, 1991.

[11] R. T. Tung, "Electron transport at metal-semiconductor interfaces: general theory," *Physical Review B*, vol. 45(23), pp. 13509-13522, 1992.

[12] S. Chand and J. Kumar, "Evidence for the double distribution of barrier heights in Pd<sub>2</sub>Si/n-Si Schottky diodes from I-V-T measurements," *Semiconductor Science and Technology*, vol. 11, pp. 1203-1208, 1996.

[13] Ö. S. Aniltürk, R. Turan, "Electrical transport at a nonideal CrSi2-Si junction," *Solid State Electronics*, vol. 44, pp. 41-48, 2000.

[14] M. Gülnahar and H. Efeoğlu H, "Double barrier nature of Au/p-GaTe Schottky contact: Linearization of Richardson plot," *Solid State Electronics*, vol. 53, pp. 972-978, 2009.

[15] M. Gülnahar and H. Efeoğlu, "Multiple-barrier distribution behavior of Mo/p-GaTe fabricated with sputtering," *Journal of Alloys and Compounds*, vol. 509, pp. 7317-7323, 2011.

[16]. M. Gülnahar, H. Efeoğlu, M. Şahin M, "On the studies of capacitance-voltage-temperature and deep level characteristics of an Au/p-GaTe Schottky diode," *Journal of Alloys and Compounds*, vol. 694, pp. 1019-1025, 2017.

[17]. M. Gülnahar M, T. Karacali T, H. Efeoğlu, "Characterization of electrical transport and properties of an Al/porous Si (PS)/p-Si/Al heterojunction," *Journal of Alloys and Compounds*, vol. 797, pp. 859-864, 2019.

[18]. M. Gülnahar, H. Nasser, A. Salimi, R. Turan, "On the electrical and charge conduction properties of thermally evaporated MoOx on n- and p-type crstalline silicon," *Journal of Materials Science-Materials in Electronics*, vol. 32, pp. 1092-1104, 2021.

[19] H. C. Card and E. H. Rhoderick, "Studies of tunnel MOS diodes I. Interface effects in silicon Schottky diodes," *Journal of Physics D: Applied Physics*, vol. 4, pp. 1589-1601, 1971.

[20] J. Werner, K. Ploog and H. J. Queisser, "Interface-state measurements at Schottky contacts: A new admittance technique," *Physical Review Letters*, vol. 57, pp. 1080-1083, 1986.

[21] J. Werner, A. F. J. Levi, R. T. Tung, M. Anzlowar and M. Pinto, "Origin of the excess capacitance at intimate Schottky contacts," *Physical Review Letters*, vol. 60, pp. 53-56, 1988.

[22]. M. Bhatnagar, B. J. Baliga, *IEEE Trans Electron Devices*, vol. 40, pp. 645-655, 1993.

[23]. H. Morkoç, S. Strike, G. B. Gao, M. E. Lin, B. Sverdlov, M. Burns, *J Applied Physics*, vol. 76, pp. 1363, 1994.

[24]. C. E. Weitzel, *IEEE Electron Device Letters*, vol. 16, pp. 451-453, 1995.

[25]. J. B. Casady, and R. W. Johnson, *Solid State Electronics*, vol. 39, pp. 1409-1422, 1996.

[26]. C. E. Weitzel, J. W. Palmour, Jr C. H. Carter, J. K. Moore, K. J. Nordquist, S. Allen, C. Thero and M. Bhatnagar, *IEEE Trans Electron Devices*, vol. 43, pp. 1732-1741, 1996.

[27]. M. C. Gupta, J. Ballato, *The Handbook of Photonics*, second ed., CRC Press London, New York, 2006.

[28]. F. Roccaforte, C. Bongiorno, F. La Via, V. Raineri, *Applied Physics Letters*, vol. 85, pp. 6152-6154, 2004.

[29]. P. A. Ivanov, A. S. Potapov, T. P. Samsonova, *Semiconductors*, vol. 43, pp. 185-188, 2009.

[30]. F. Roccaforte, F. La Via, V. Raineri, R. Pierobon, E. Zanoni, *Journal of Applied Physics*, vol. 93, pp. 9137-9144, 2003.

[31]. L. Zhang, Y. M. Zhang, Y. M. Zhang, C. Han, Y. J. Ma, *Chinese Physics B*, vol. 18, pp. 3490-3494, 2009.

[32]. L, Boussouar, Z. Ouennoughi, N. Rouag, A. Sellai, R. Weiss, H. Ryssel, *Microelectronic Engineering*, vol. 88, pp. 969-975, 2011.

[33]. Latreche A, Ouennoughi Z, Sellai A, Weiss R, Ryssell H, 2011. Semiconductor Science and Technology, 26, (085003), 1-9.

[34]. S. Toumi, A. Ferhat-Hamida, L. Boussouar, A. Sellai, Z. Ouennoughi, H. Ryssel, *Microelectronic Engineering*, vol. 86, pp. 303-309, 2009.

[35]. R. Perez, N. Mestres, D. Tournier, P. Godignon, J. Millan, *Diamond and Related Materials*, vol. 14, pp. 1146-1149, 2005.

[36]. K. Çınar, C. Coşkun, E. Gür, Ş. Aydoğan, *Nuclear Instruments and Methods in Physics Research B*, vol. 267, pp. 87-90, 2009.

[37]. M. Ben Karoui, R. Gharbi, N. Alzaied, M. Fathallah, E. Tresso, L. Scaltrito, S. Ferrero, *Solid-State Electronics*, vol. 52, pp. 1232-1236, 2008.

[38]. C. F. Pirri, S. Ferrero, L. Scaltrito, D. Perrone, S. Guastella, M. Furno, G. Richieri, L. Merlin, *Microelectronic Engineering*, vol. 83, pp. 86-88, 2006.

[39]. F. Roccaforte, C. Bongiorno, F. La Via, V. Raineri, *Applied Physics Letters*, vol. 85, pp. 6152-6154, 2004.

[40]. F. Roccaforte, F. La Via, V. Raineri, R. Pierobon, E. Zanoni, *Journal of Applied Physics*, vol. 93, pp. 9137-9144, 2003.

[41]. L. Zhang, Y. M. Zhang, Y. M. Zhang, C. Han, Y. J. Ma, "High energy electron radiation effect on Ni/4H-SiC SBD and Ohmic contact," *Chinese Physics B*, vol. 18, pp. 3490-3494, 2009.

[42]. L. Boussouar, Z. Ouennoughi, N. Rouag, A. Sellai, R. Weiss, H. Ryssel, *Microelectronic Engineering*, vol. 88, pp. 969-975, 2011.

[43]. A. Latreche, Z. Ouennoughi, A. Sellai, R. Weiss, H. Ryssell, *Semiconductor Science and Technology*, vol. 26 (085003), pp. 1-9, 2011.

[44]. W. R. Harrell, J. Zhang, K. F. Poole, *Journal of Electronic Materials*, vol. 31, pp. 1090-1095, 2002.

[45]. Zhang J and Harrell WR, 2003. J Vac Sci Tech B, 21, 872-878.

[46]. S. Doğan, D. Johnstone, F. Yun, S. Sabuktagin, J. Leach, A. A. Baski, H. Morkoç, G. Li, B. Ganguly, *Applied Physics Letters*, vol. 85, pp. 1547-1549, 2004.

[47]. E. H. Rhoderick and R. H. Williams, *Metal-Semiconductor Contacts*, 2<sup>nd</sup> ed., Oxford: Clarendon, 1988.

[48] S. K. Cheung and N. W. Cheung, *Applied Physics Letters*, vol. 49, pp. 85, 1986.

[49]. S. M. Sze, *Physics of Semiconductor Devices*, second edition, John Wiley and Sons, New York, 1981.

[50]. R. F. Broom, H. P. Meier, and W. Walter W, *Journal of Applied Physics*, vol. 60, pp. 1832-1833, 1986.

[51]. Zs J. Horvath, Solid State Electronics, vol. 39, pp. 176, 1996.