Uluslararası İleri Doğa Bilimleri ve Mühendislik Araştırmaları Dergisi Sayı 7, S. 473-476, 10, 2023 © Telif hakkı IJANSER'e aittir **Araştırma Makalesi** 



https://alls-academy.com/index.php/ijanser ISSN: 2980-0811 International Journal of Advanced Natural Sciences and Engineering Researches Volume 7, pp. 473-476, 10, 2023 Copyright © 2023 IJANSER **Research Article** 

**A Compact Microstrip Planar Crossover for LTE Applications** 

Mert Yerli<sup>1\*</sup>, Merih Palandöken<sup>2</sup>

<sup>1\*</sup>Izmir Katip CelebiUniversity, Faculty of Engineering and Architecture, Departmant of Electrical and Electronics Engineering, İzmir, Turkey, (ORCID: 0000-0003-2088-1776)

<sup>2</sup>Izmir Katip CelebiUniversity, Faculty of Engineering and Architecture, Departmant of Electrical and Electronics Engineering, İzmir, Turkey, (ORCID: 0000-0003-3487-2467)

\*( <u>eng.mertyerli@gmail.com</u>) Email of the corresponding author

(Received: 21 November 2023, Accepted: 27 November 2023)

(4th International Conference on Engineering and Applied Natural Sciences ICEANS 2023, November 20-21, 2023)

**ATIF/REFERENCE:** Yerli, M. & Palandöken, M. (2023). A Compact Microstrip Planar Crossover for LTE Applications. *International Journal of Advanced Natural Sciences and Engineering Researches*, 7(10), 473-476.

*Abstract* – Microwave integrated circuits (MICs) are becoming increasingly important in communication systems due to their ability to efficiently transfer power and signals using crossover structures. Conventional crossover designs, however, suffer from significant signal and power losses, limiting their performance. In various studies, there are complex planar designs, MICs and Radio Frequency (RF) circuits operating from frequencies and bands. This paper proposes a novel fully symmetric four-port microstrip planar crossover that overcomes these limitations. A square microstrip structure with a size of  $80.0 \times 80.0 \times 1.6 \text{ mm}^3$  is presented in the design, which has a planar crossover and wide band gap at an operating frequency of 2.1 GHz. The proposed design provides excellent isolation between ports and minimizes signal and power losses, making it a promising solution for MICs and RF circuits for LTE applications.

## Keywords – Microwave Integrated Circuits (MICs), Crossover, Radyo Frequency (RF), Microstrip Structures, LTE Applications

## I. INTRODUCTION

Microwave integrated circuits (MICs) technology, which is one of the most important technologically innovative areas in power transfer and signal transfer with the help of crossover structures, is becoming one of the fastest and most innovative growing technological trends in the field of communication in addition to the conventional mmwave components [1-5]. In parallel with the innovative developments in MICs, the use of these crossover structures is increasing, especially in structures that have two signal paths where the ports in the circuit match and overlap each other with perfect isolation. In the past, wire bond or air bridge [6-8] structures were used to allow two signals to pass each other. Considering that these structures cause significant signal and power losses and slow transmission, planar structures [9-14,17] as a new perspective have become very important. Calculated and measured values between lines and ports at the operating frequencies of Radio Frequency (RF) elements are carefully evaluated in terms of system bandwidth and lossless transmission. With the beginning of studies on structures to change the frequency range and increase the bandwidth of conventional MICs [9], there is a new interest in the design and analysis of both single and dual-band structures in these circuits and gates operating at different frequencies [7, 10-15,17].

Nowadays, with the increasing interest in planar technology, the complexity of RF circuits and MICs

has increased rapidly. As a result of the innovation and diversification of high-density microwave integrated circuits and other microwave planar systems containing many components, crossovers have become important components in various microwave circuits. These microstrip planar crossovers, which are used in communication technologies today, are recommended as planar solutions due to their low profile, cheap cost, ease of production and low weight. The relatively more complex structure of a gate is an obstacle to such an effort.

With this purpose in this article proposes the fully symmetric four-port microstrip planar crossover introduced in the Fig. 1, which leads to a much-simplified analysis of the crossovers. In the crossover parameters, the insertion loss, return loss and isolation parameters measured between the ports were calculated according to the proposed symmetric structure. This structure prevents signal and power losses and provides perfect isolation. Moreover, the agreement between calculations and simulation results confirms the validity of the proposed structure for LTE applications.

Section II details the planar crossover design methodology and various parameters used in the study, while Sections III-IV show and discuss the simulation results of the designed crossover. Section V summarizes the result of the work focused on designing planar crossovers for RF circuits and MICs.

## II. MATERIALS AND METHOD

There are various crossover designs used for RF and microwave integrated circuit systems [10-15]. In this study, a planar, single-band crossover design was designed. The proposed microstrip planar crossover was produced on FR-4 substrate with dielectric constant ( $\varepsilon_r$ ), loss tangent ( $tan \delta$ ) and layer thickness (h) of 4.3, 0.025 and 1.6 mm, respectively. The overall size of this crossover is  $80.0 \times 80.0 \times 1.6 \text{ mm}^3$ .

The proposed planar crossover design includes many geometric parameters, and a connection structure is created through four ports, two of which are well isolated, including the output and input. The structure in Fig.1 was designed on CST Studio Suite through the electromagnetic (EM) simulation program and the parameters are listed in Table 1.



Fig. 1 Proposed planar crossover design

Table 1. Parameter of proposed planar crossover (mm)

W	X1	X2	X3	X4
80.0	52.0	3.8	20.4	13.6
L	Y1	Y2	¥3	¥4
80.0	8.1	6.5	9.8	8.2

# III. RESULTS

In planar crossover designs, frequency and band gaps are important in structures designed differently for signal and power transmission. To simulate the frequency responses, a 2.1 GHz microstrip planar crossover is numerically simulated based on the design as an example, the results of which are shown in Fig. 1. We can conclude that the proposed design has a low insertion loss around 2.1 GHz, shown as  $S_{31}$  in Fig. 2. A result of -0.17 dB at this frequency (lower than 0.5 dB, including the port connector loss) indicates that the loss is very low, and it transmits well. Similarly, it also provides high isolation for Port 2 and 4 over the same band.



Fig. 2  $S_{31}$  for proposed planar crossover

In Fig. 3, it appears that at the 2.1 GHz frequency, the power reflected to the input is almost absent at the -33.05 dB value. This return loss is below -10 dB and is close to ideal results for the proposed design.



Fig. 3  $S_{11}$  for proposed planar crossover

Fig. 4 shows the  $S_{21}$  parameter characteristics of the planar crossover and it is clear that with a remarkable bandwidth at 2.1 GHz, losses are less than -45 dB for future operation in this band for use in higher RF and microwave integrated circuits.



Fig. 4  $S_{21}$  for proposed planar crossover

Another important point is that it also appears to operate over a wide bandwidth between 2.0 GHz and 2.3 GHz below -10 dB. Here it shows that the signal applied to the input port is well isolated from the Port 2.

### **IV. DISCUSSION**

In this paper, a crossover is designed to reduce the complexity of complex RF circuits and MICs provide easy analysis and provide a planar solution in LTE applications. In the proposed study, it has a wide operating bandwidth and offers diversity in terms of size and design among different forms compared to previous studies. Ensuring isolation and lossless signal transfer coincide with the findings. Thus, the results presented improved methods for the applicability of the proposed design concept and diagonal structure.

## **V. CONCLUSION**

The study has the main theoretical contributions to the literature. Primarily, this study is an innovative study that analyses the MICs and RF circuits complexity by finding a solution, taking into account that it has a planar structure that is easy to manufacture, reduces signal losses and provides a very good level of power transmission.

#### ACKNOWLEDGMENT

This study has been carried out using the laboratory of Izmir Katip Celebi University Smart Factory Systems Application and Research Center (AFSUAM).

## REFERENCES

- Rymanov, Vitaly, et al. "Integrated photonic 71–76 GHz transmitter module employing high linearity double mushroom-type 1.55 μm waveguide photodiodes." 2012 IEEE International Topical Meeting on Microwave Photonics. IEEE, 2012.
- [2] Rymanov, Vitaly, et al. "Integrated E-band photoreceiver module for wideband (71–76GHz) wireless transmission." 2012 7th European Microwave Integrated Circuit Conference. IEEE, 2012.
- [3] Bouhlal, Bouchaib, et al. "Integration platform for 72-GHz photodiode-based wireless transmitter." RF and Millimeter-Wave Photonics II. Vol. 8259. SPIE, 2012.
- [4] Palandöken, Merih, et al. "Compact metamaterial-based bias tee design for 1.55 μm waveguide-photodiode based 71–76GHz wireless transmitter." Progress in Electromagnetics Research Symposium, PIERS. 2012.
- [5] Montero-de-Paz, Javier, et al. "Compact modules for wireless communication systems in the E-band (71–76 GHz)."Journal of Infrared, Millimeter, and Terahertz Waves 34 (2013): 251-266.
- [6] S. C. Wu, H. Y. Yang, N. Alexopoulos, and I. Wolff, "A rigorous dispersive characterization and microstrip cross and T junctions," IEEE Trans. Microw. Theory Techn., vol. 38, no. 12, pp. 1837–1844, Dec. 1990.
- [7] Y. -Y. Lin, B. -L. Jiang, C. -Y. Huang and C. -W. Tang, "Design of a wideband planar patched crossover with slotted lines," 2017 IEEE Asia Pacific Microwave Conference (APMC), Kuala Lumpur, Malaysia, 2017, pp. 280-282, doi: 10.1109/APMC.2017.8251433.
- [8] T. S. Hong, "A rigorous study of microstrip crossovers and their possible improvements," IEEE Trans. Microw. Theory Techn., vol. 42, no. 9, pp. 1802–1806, Sep. 1994
- [9] Zhao, Y., W. Feng, T. Zhang, W. Che, and Q. Xue, "Planar single/dual-band crossovers withlarge-frequency ratios using coupled lines," IEEE Microw. Wireless Compon. Lett., Vol. 27, No. 10,870–872, Oct. 2017
- [10] H. Ren, M. Zhou, H. Zhang and B. Arigong, "A Novel Dual-Band Zero-Phase True Crossover with Arbitrary Port Impedance," in IEEE Microwave and Wireless Components Letters, vol. 29, no. 1, pp. 29-31, Jan. 2019, doi: 10.1109/LMWC.2018.2882094.
- [11] J. Shao, H. Ren, B. Arigong, C. Li and H. Zhang, "A Fully Symmetrical Crossover and Its Dual-Frequency Application," in IEEE Transactions on Microwave Theory and Techniques, vol. 60, no. 8, pp. 2410-2416, Aug. 2012, doi: 10.1109/TMTT.2012.2198229.
- [12] Purnima G and S. K. Menon, "Microstrip patch based high isolation planar crossover for beamforming applications," 2016 IEEE International WIE Conference on Electrical and Computer Engineering (WIECON-ECE), Pune, India, 2016, pp. 184-187, doi: 10.1109/WIECON-ECE.2016.8009114.

- [13] C. -W. Tang, K. -C. Lin and W. -C. Chen, "Analysis and Design of Compact and Wide-Passband Planar Crossovers," in IEEE Transactions on Microwave Theory and Techniques, vol. 62, no. 12, pp. 2975-2982, Dec. 2014, doi: 10.1109/TMTT.2014.2364839.
- [14] C. -H. Pai and Y. -H. Pang, "A Balanced Filtering Crossover for Two Paths with Different Frequencies," 2020 International Symposium on Antennas and Propagation (ISAP), Osaka, Japan, 2021, pp. 31-32, doi: 10.23919/ISAP47053.2021.9391520.
- [15] Yerli, M., & Palandöken, M. (2023). "Γ Shaped Slotted Compact Circular Crossover for Communication Applications at 2.45 GHz." International Journal of Advanced Natural Sciences and Engineering Researches, 7(7), 115–119. https://doi.org/10.59287/ijanser.1345
- [16] D. M. Pozar, "Microwave engineering", John wiley & sons, 2011
- [17] B. Xue, K. Haneda and C. Icheln, "Improving the Patch-Based Crossovers by Parasitic Modes and a Sub-Element Technique," in IEEE Transactions on Circuits and Systems II: Express Briefs, vol. 69, no. 9, pp. 3774-3778, Sept. 2022, doi: 10.1109/TCSII.2022.3177928.