Uluslararası İleri Doğa Bilimleri ve Mühendislik Araştırmaları Dergisi Sayı 7, S. 21-26, 6, 2023 © Telif hakkı IJANSER'e aittir **Araştırma Makalesi**



International Journal of Advanced Natural Sciences and Engineering Researches Volume 7, pp. 21-26, 6, 2023 Copyright © 2023 IJANSER **Research Article**

https://as-proceeding.com/index.php/ijanser ISSN: 2980-0811

Online Failure Detection using Deep Learning in FPGA PCB Interface

Afaq Ahmad¹*, Mohamed Abdul Karim², Ahmed Al Maashri³, Medhat Awadalla⁴,

Sayyid Samir Al Busaidi⁵ and Maram Ahmed Al Khuzaimi⁶

¹Department of Electrical & Computer Engineering, Sultan Qaboos University, Oman ^{2.6}Department of Information Technology, University of Technology and Applied Sciences, Oman ^{3, 4, 5}Department of Electrical & Computer Engineering, Sultan Qaboos University, Oman

*(afaq@squ.edu.om)

(Received: 29 June 2023, Accepted: 13 July 2023)

ATIF/REFERENCE: Ahmad, A., Karim, M. A., Maashri, A. A., Awadalla, M., Busaidi, S. S. & Khuzaimi, M. A. (2023). Online Failure Detection using Deep Learning in FPGA PCB Interface. *International Journal of Advanced Natural Sciences and Engineering Researches*, 7(6), 21-26.

Abstract – This research paper is aimed to present a real-time failure detection technique while working with Field Programmable Gate Arrays (FPGA) and interfaced Printed Circuit Boards (PCBs). In this research, we explored the feasibility of currently available innovative Deep Learning (DL) algorithms to detect the defects in variety of PCBs. In our proposed technique, we trained the YOLOv5 (You Only Look Once) algorithm with a few hundreds of defective PCBs' images, which were obtained from Kaggle, an online community of data scientists and machine learning practitioners. The advantage of using YOLOv5 is that the detection is carried out in real-time. In the next phase, after training, the algorithm undergoes validation and testing, where we tested with different images. The obtained results are promising, as the Deep Learning process successfully detects the defects on the PCBs.

Keywords – Deep Learning, FPGA, PCB Defect Detection, YOLOv5, Kaggle

I. INTRODUCTION

Embedded Vision Systems (EVSs) need guided Machine Learning (ML) and Computer Vision (CV) acceleration. Systems on Chips (SoCs) with programmable logic are an essential element of realtime EVSs. Although introducing CV to EVSs' design can be a complex process. Hence, the design strategy of Field Programmable Gate Arrays (FPGAs) is changing, especially in the sphere of EVSs. In current scenario, the FPGAs industries [1-7] provide a fully integrated solution that engineer can modify and build upon. Software engineers can get started on complex Machine Learning (ML) based image-processing designs. The new design solutions utilize software-based systems with integrated hardware acceleration, allowing faster development but also requiring new design methods and tools [8-10].

Embedded Vision Systems' Printed Circuit Boards (PCBs) have become integral to all electronic devices due to the diverse specification needs Therefore, for universal success and acceptance of the of users' requirements. Integrated PCB design requires being defect free. Printed Circuit Boards are manufactured on large scale and could be prone to certain defects, over a period. Defects in PCBs such as shorts, spurs, mouse bites, and pinholes thus cause issues like current leakage and open circuits, which in turn quickly either degrading performance or rendering the defective PCBs useless in respect to its application.

Different Fault-Tolerance (FT) techniques [11-14] are employed to ensure that defects are kept to a minimum and that to increase the reliability and dependability of applications on ICs [1-14]. However, inspecting and diagnosing defects manually is a challenging aspect. It is timeconsuming, expensive, and subjective, as in any field, calling for automation. In addition, PCBs can be extremely detailed and technically demanding, cramming multiple critical components into a small package. Thus, considering requirement of ensuring reliability and constrained challenges, an automated solution for detecting and classifying defects in PCBs is in demand.

Artificial Intelligence (AI) is becoming increasingly popular with the success of Deep Learning (DL) in many industrial applications. Some of these have been solving image analysis and segmentation problems to classify images and discover anomalies. This research employs DL techniques to identify defects in the PCBs. The background, implementation details, results, and conclusions will be presented in this paper.

II. RELATED WORK

Broadly, the PCB defect detection techniques can be grouped in three categories, as mentioned below:

- Image processing-based techniques,
- Machine learning based techniques, and
- Object detection techniques.

The first technique is based on a MATLAB image-processing technique to discover and classify 14 different types of defects (see Table 1) in PCB [15]. The authors in this work [15], used a defectfree PCB's template, which is used as a reference image to map with the defected PCB image and if any difference is found after subtraction then the inspected image is classified as defective. The technique primarily uses morphology to discover the differences by comparing every pixel value between two images. The difference between two images (x, y) and h(x, y) is expressed as in Equation (1). Consideration of outputs g(x, y) used are just in negative and positive pixel image, since zeros values of data do not affect the output of the operation.

$$(x, y) = (x, y) - h(x, y)$$
 (1)

In another research based on morphological segmentation approach, image a unique methodology have been presented for detecting the defects in PCBs and classify them as per defects [16]. This approach is based on image segmentation algorithm together with image processing theories. Printed Circuit Board defects have been identified using its morphology that is based on the structure or form of objects. Morphological filtering simplifies a segmented image, making it easier to find objects of interest. This is accomplished by blasting out the item contour, filtering small holes, and removing small projections. Morphology is implemented using MATLAB software [17]. Another morphological technique involving image subtraction operation compares a conventional PCB image with a PCB image to be inspected and discovers the faulty region [18].

III. METHODOLOGY AND METHOD

A dataset of PCB images has been used to train a deep neural network [19]. The dataset has 1386 images classified into six defects listed below:

- Missing hole,
- Mouse bite,
- Open circuit,
- o Short,
- Spur and
- Spurious copper.

Each of these types of defects are represented by at least 115 images with different positions and angles to denote possible faults.

In this paper, we built a convolutional neural network that is trained on 692 labeled images classified into the above mentioned six classes. The images are obtained from Kaggle [20]. Subsequently, distinct sets of images are used for testing and validation. The distribution chosen for training, validation and testing sets is 70, 20 and 10 percents respectively.

A. Kaggle

Kaggle is a crowd-sourced platform to attract, nurture, train and challenge data scientists from all around the world to solve data science, machine learning and predictive analytics problems. It has over 536,000 active members from 194 countries and it receives close to 150,000 submissions per month [20].

```
B. YOLOv5
```

The acronym YOLO is abbreviated for You Only Look Once. YOLO is an object detection algorithm that divides images into a grid system. Each cell in the grid is responsible for detecting objects within itself. In our work we used YOLO v5 for the object detection purpose as it is one of the best deep learning algorithms that is publicly available since the end of 2020 [21]. The key advantages of this algorithm are that it is faster than previous versions and that it is more accurate. We apply this technique to a relatively new domain viz. PCB quality check. Visually detecting the flaws in PCBs is a skilled labor-intensive task as there are a variety of problems. Scrutinizing a board's working condition involves testing tools such as multi-meter, thermal cameras, magnifying glass, and oscilloscope. If the appropriate defects are not detected in a timely manner, the whole PCB may be damaged. Our algorithm could predict most of the defects as shown in the results section. The application of such a system is convenient as users need to just upload an image of PCB and the algorithm detects if there is any defect in the image.

C. Deep Learning Approach

Deep learning [22] is considered as one of the powerful advances in machine learning research field and is used to simulate and develop the human brain concept and then interpret and analyze data such as image, voice and text [23, 24]. The success reason of traditional machine learning depends upon the handcrafted feature representation performance. Through this process, the aim of machine learning approaches is only to optimize learning weights and ultimately to achieve the optimal learning outcomes [25]. Different from the traditional machine learning methods, deep learning automatically tries to get the work of data representation and feature extraction completed [26, 27]. The classification and labelling images accuracy using deep learning outperforms any other traditional algorithms and get remarkable results that cannot be obtained using humans. Google nowadays uses DL to manage and control the energy at data centers of the company. More than 40 percent of their need of the energy is reduced for cooling. In turns, hundreds of millions of dollars are saved for the company and achieved more than 15 percent in power reduction. Robust and open-source software packages have been widely spread due to the advances in practical applications of deep learning. In addition, it urges the people to push the development forward.

D. Convolutional Network

Convolutional Neural Network (CNN) is a neural network type that has the capability to identify characteristics of the nature (features) of the input image. This method serves to extract the raw image into a classified image. The layer that is used to serve for extracting the image features is called a convolutional layer [28]. Convolution Neural Network has four layer architectures [29]. Namely, these layers are as mentioned below:

- Convolutional layer,
- o Activation function,
- \circ Pooling, and
- Fully connected layer

The Convolutional Neural Network power comes from the deep architecture, which extracts a set of discriminating feature representations at different levels of abstraction. Recently, it has been widely used in classification and recognition due to its remarkable performance [30, 31]. Its ability to learn rich image features is the main reason of its success. However, to train a deep CNN network needs plenty of labeled datasets for pre-training and optimize its hyper-parameters [32]. Convolutional Neural Networks are category of deep neural networks used especially in computer vision area such as image classification [33, 34].

IV. EXPERIMENTS AND RESULTS

As introduced earlier, the dataset contains images of PCBs released by the Open Lab on Human Robot Interaction of Peking University. The dataset is synthetic as it is edited with Photoshop. It has different images classified based on the defects. Center the following figures show some samples from the dataset, such as a missing hole is shown in Figure 1.

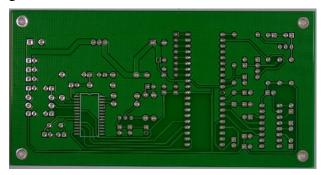


Fig. 1. PCB image with missing hole

The dataset also include a set of rotated images to enable the learning algorithm to demonstrate tolerance. The image rotation is performed randomly using a Python code that adjusts the position of image center when rotation is performed. An example of rotated image is shown in Figure 2, while Figure 3 depicts a raw image with another defect viz. spurious copper.

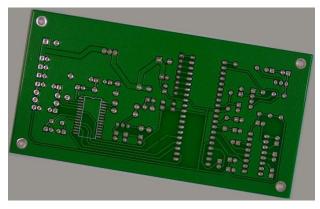


Fig. 2. Rotated image of PCB

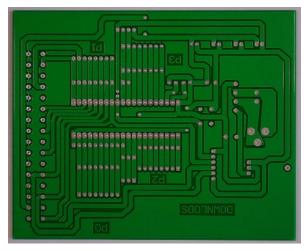
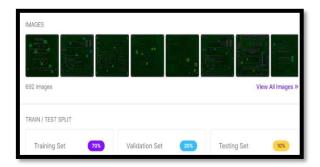


Fig. 3. PCB image with spurious copper

Roboflow is used to pre-process and organize the dataset. A screenshot of this is shown in Figure 4. YOLOv5 belongs to a family of object detection architectures that are pre-trained and open-source based on PyTorch and TensorFlow. Google Colab is

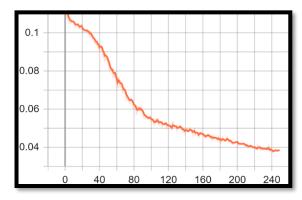


the environment employed to conduct the experiments.

Fig. 4. Screenshot of distribution

The system instantly identifies the defects on PCB by drawing a bounding box around each defect. The accuracy is assessed based on the number of such boxes drawn automatically to the actual defects. TensorBoard is the visualization toolkit that has been used to depict the following charts.

Figure 5 plots the loss of fitting the bounding boxes. It shows that the error decreases over a period with an increase in the number of images. The chart



in Figure 6 shows that the classification error decreases. As the training sample size increases, the rate of misclassification is reduced.

Fig. 5. Missing the bounding boxes around defects in the images

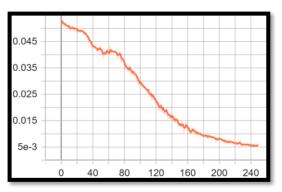


Fig. 6. Classification of error

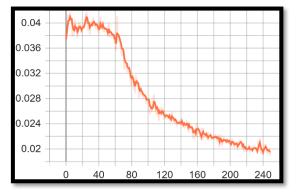


Fig. 7. Object loss in learning process

V. CONCLUSION

This paper presented an innovative application of Computer Vision to detect the flaws in PCBs. A Deep Learning model was built based on variety of training samples from Kaggle. The system was able to identify the defects precisely. The advantage of using YOLOv5 is that the detection is carried out in real-time. Hence, it can instantaneously spot the defects by drawing a bounding box around the defective part. Moreover, the accuracy of the learning model encourages to further explore Embedded Vision Systems.

ACKNOWLEDGMENT

The acknowledgements are due to Authorities of Sultan Qaboos University (Sultanate of Oman) for providing generous research support grants and environments for carrying out the research works. This research work was supported by the internal grant of Sultan Qaboos University (number IG/ENG/ECED/20/01).

REFERENCES

- [1] (2023) Intel® FPGAs and Programmable Devices.
 [Online]. Available: https://www.intel.com/content/www/us/en/products/programmable.html
- [2] (2023) Intel® AgilexTM FPGA. [Online]. Available: https://www.intel.com/content/www/us/en/products/det ails/fpga/agilex/f-series.html
- [3] (2023) Intel® Agilex[™] F-Series 027 FPGA. [Online]. Available: https://ark.intel.com/content/www/us/en/ark/products/2 08599/intel-agilex-fseries-027fpga-r25a.html
- [4] (2023) Adaptive Computing, Xilinx. [Online]. Available: https://www.xilinx.com/applications/adaptivecomputing.html
- (2023) FPGAs & 2D ICs, Xilinx. [Online]. Available: https://www.xilinx.com/products/silicondevices/fpga.html
- [6] (2023) Virtex UltraScale+, Xilinx. [Online]. Available:

https://www.xilinx.com/products/silicondevices/fpga/virtex-ultrascale-plus.html

- [7] A. Ahmad, S. S. Al-Busaidi, A. Al-Maashri, M. Awadalla and S. Hussain, "FPGAs - chronological developments and challenges," *International Journal of Electrical Engineering and Technology (IJEET)*, vol. 12, Issue 11, pp. 60-72, 2021.
- [8] A. Ahmad, "Automotive semiconductor industry trends, safety and security challenges," in Proc. 8th International Conference on Reliability, Infocom Technologies and Optimization (Trends and Future Directions) (ICRITO), Noida, India, June 2020, pp. 1373-1377.
- [9] A. Ahmad, "Reliable and fault tolerant systems on chip through design for testability," in *Proc. 2019 Amity International Conference on Artificial Intelligence –* (AICAI'19), Dubai, UAE, Feb. 4-7, 2019, pp. 50-53.
- [10] A. Ahmad, "Challenges for test and fault-tolerance due to convergence of electronics, semiconductor systems and computing," in Proc. 2017 International Conference on Infocom Technologies and Unmanned Systems (Trends and Future Directions) (ICTUS), Amity University, UAE, Dec. 18-20, 2017, pp. 64-68.
- [11] A. Ahmad and D. Ruelens, "Development of digital logic design teaching tool using MATLAB & SIMULINK, *IEEE Technology and Engineering Education (ITEE)*, vol. 8, no. 1, 2013, pp. 7-11.
- [12] A. Ahmad, D. Al-Abri and S. S. Al-Busaidi, "Adding pseudo-random test sequence generator in the test simulator for DFT approach," *Journal Computer Technology and Applications, David Publishing (USA)*, vol. 3, no. 7, 2012, pp. 463-470.
- [13] N. K. Nanda, A. Ahmad and V. C. Gaindhar, "Shift register modification for multipurpose use in combinational circuit testing," *Int'l Journal of Electronics* (*UK*), vol.66, no.6, 1989, pp. 875-878.
- [14] A. Ahmad and N. K. Nanda, "Effectiveness of multiple compressions of multiple signatures, *Int'l Journal of Electronics (UK)*, vol.66, no.5, 1989, pp.775-787.
- [15] K. Kamalpreet and K. Beant, "PCB defect detection and classification using image processing," *International Journal of Emerging Research in Management & Technology*, vol. 3, no. 8, 2014, pp. 1-10
- [16] S. H. I. Putera and Z. Ibrahim, "Printed circuit board defect detection using mathematical morphology and MATLAB image processing tools," in *Proc. 2nd Int. Conf. on Education Technology and Computers (ICETC 2010)*, Shanghai, China, 2010, pp. 359-363.
- [17] R. C. Mat, S. Azmi, R. Daud, A.N. Zulkifli, and F. K. Ahmad, "Morpholocal operation on printed circuit board (PCB) reverse Engineering using MATLAB," in Proc. of Knowledge Management International Conference and Exhibition, Legend Hotel Kuala Lumpur, Malaysia, June 6-8, 2006, pp. 529-533.
- [18] F. B. Nadaf and V. S. Kolkure, "Detection of bare PCB defects by using morphology technique," *International Journal of Electronics and Communication Engineering*. vol. 9, no. 1, 2016, pp. 63-76.
- [19] W. Huang, P. Wei, M. Zhang and H. Liu, "HRIPCB: a challenging dataset for PCB defects detection and

classification," in *Proc. The 3rdAsian Conference on Artificial Intelligence Technology (ACAIT)*, 22 May 2020, pp. 303-309.

- [20] (2023) Kaggle: PCB defects. [Online]. Available: https://www.kaggle.com/datasets/akhatova/pcb-defects
- [21] (2023) YOLO Ver5. [Online]. Available: https://towardsdatascience.com/yolo-v4-or-yolo-v5or-pp-volo-dad8e40f7109
- [22] Y. LeCun, Y. Bengio and G. Hinton, "Deep learning," *Nature*, vol. 521, 2015, pp. 436-444.
- [23] J. Schmidhuber, "Deep learning in neural networks: an overview," *Neural Network*. vol. 61, pp. 85-117.
- [24] C. Dong, C. Loy, K. He and X. Tang, "Learning a deep convolutional network for image super-resolution," in *Proc. European Conference on Computer Vision (ECCV)*, Zurich, Swiss. September 6-12, 2014, pp. 184-199.
- [25] H. Cai, F. Yan F and K. Mikolajczyk, "Learning weights for codebook in image classification and retrieval," in *Proc. Computer Vision and Pattern Recognition (CVPR)*, San Francisco, CA, USA. June 13-18, 2010, pp. 2320-2327.
- [26] S. Chaib, H. Yao, Y. Gu and M. Amrani, "Deep feature extraction and combination for remote sensing image classification based on pre-trained CNN models," in Proc. of the Ninth International Conference on Digital Image Processing (ICDIP); Hongkong, China. 19-22 May 2017; p. 104203D.
- [27] Y. Liu, Y. Li, X. Ma and R. Song, "Facial expression recognition with fusion features extracted from salient facial areas," *Sensors*, 2017, vol. 17, p. 712.
- [28] H. Khalajzadeh, M, Mansouri and M. Teshnehlab, "Face recognition using convolutional neural network and simple logistic classifier, *Soft Comput. Ind. Appl.*, 2014, pp. 197-207.
- [29] R. Yamashita, M. Nishio, R. Gian Do and K. Togashi, "Convolutional neural networks: an overview and application in radiology," *Insights Imaging*, 2018, vol. 9, pp. 611-629.
- [30] J. Lu, G. Wang and J. Zhou, "Simultaneous feature and dictionary learning for image set based face recognition," *IEEE Trans. Image Processing*, vol. 26, 2017, pp. 4042-4054.
- [31] G. Hu, X. Peng, Y. Yang, T. M. Hospedales and J. Verbeek, "Frankenstein: Learning deep face representations using small data," *IEEE Trans. Image Processing*, vol. 27, 2018, pp. 293-303.
- [32] M. Oquab M., L. Bottou and I. Laptev, "Learning and transferring mid-level image representations using convolutional neural networks," in *Proc. Computer Vision and Pattern Recognition (CVPR)*. Columbus, OH, USA. June 24–27, 2014, pp. 1717-1724.
- [33] S. Setiowati, EL. Franita and I. Ardiyanto, "A review of Optimization method in face recognition: comparison deep learning and non-deep learning methods," in *Proc.* 2017 ninth International Conference on Information Technology and Electrical Engineering (ICITEE), Phuket, Thailand, 2017, pp. 1-6.
- [34] M. Li, C. Yu, F. Nian and X. Li, "A face detection algorithm based on deep learning," *International Journal*

of Hybrid Information Technology," vol. 8, no. 11, 2015, pp. 285-296.